



Re the Application of:

MARK ANDERS

Application No.: 10/035,574

Filed: October 18, 2001

For: TRANSITION ENCODED DYNAMIC BUS

**CIRCUIT** 

Art Group: 2611

Examiner: Sudhanshu C. Pathak

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted concurrently with the Request for Continued Examination. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF TAYLOR & ZAFMAN LLP

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Date: July 10, 2006

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

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Complete if Known		
Application Number 10/035,574		
Filing Date	October 18, 2001	
First Named Inventor	Mark Anders	
Art Unit	2611	
Examiner Name	Sudhanshu C. Pathak	
Attorney Docket Number	42390P11426	

	U.S. PATENT DOCUMENTS				
Examiner Initials*	Cite No.¹	Document Number  Number - Kind Code² (if known)	Publication Date Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-6,005,417	12-21-1999	MEHTA et al.	
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	FOREIGN PATENT DOCUMENTS					
Examiner	Cite	Foreign Patent Document			Pages, Columns, Lines,	т.
	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear	1
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						-

Examiner	Date	
Signature	Considered	

\*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

'Applicant's unique citation designation number (optional). 'See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. 'Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 'For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. 'Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. 'Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

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## **INFORMATION DISCLOSURE** STATEMENT BY APPLICANT

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Complete if Known		
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Art Unit	2611	
Examiner Name	Sudhanshu C. Pathak	
Attorney Docket Number	42390P11426	

	NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", Symposium on VLSI Circuits, Digest of Technical Papers, pp. 16-17 (Jun. 13-15, 2002).			
		Anders, Mark, et al., "A Transition-Encoded Dynamic Bus Technique for High-Performance Interconnects", IEEE Journal of Solid-State Circuits, Vol. 38, No. 5, pp. 709-714 (May 2003).			
		Stan, Mircea R., et al., "Bus-Invert Coding for Low-Power I/O", IEEE Transactions on Very Large Scale Integration (VLSI) Systems, Vol. 3, No. 1, pp. 49-58 (Mar. 1995).			

Examiner	Date	
Signature	Conside	red

<sup>\*</sup>Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

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